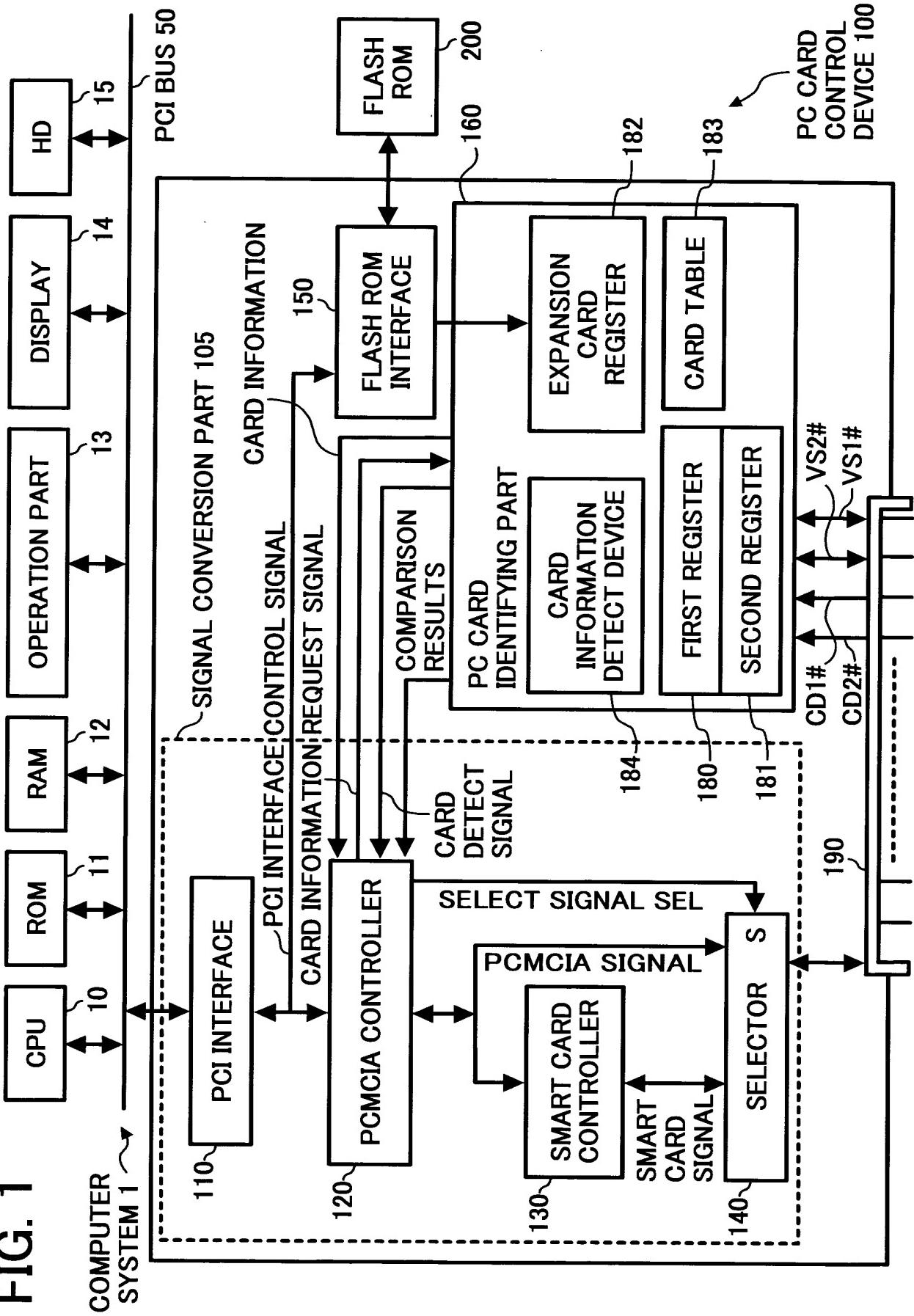
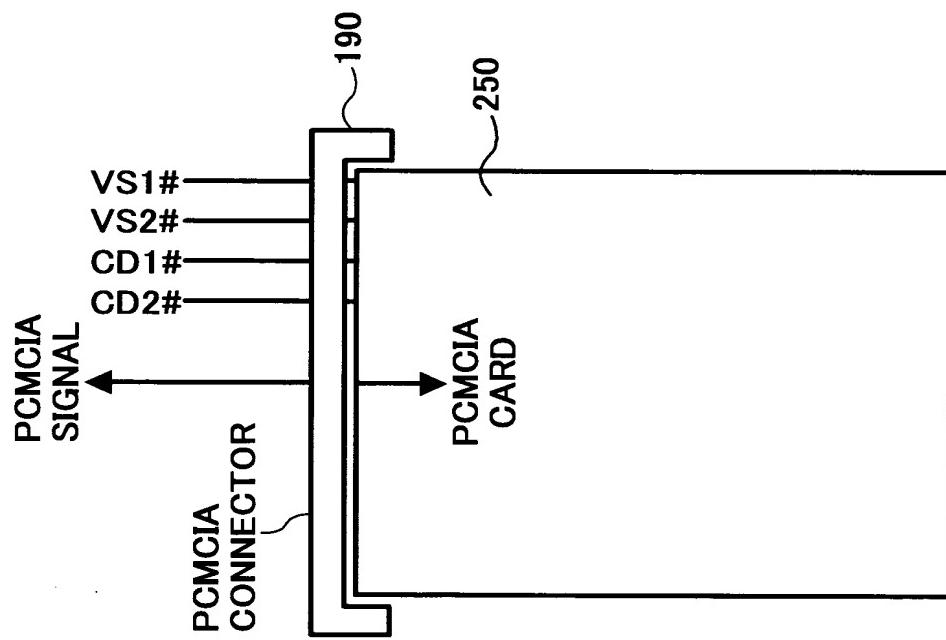


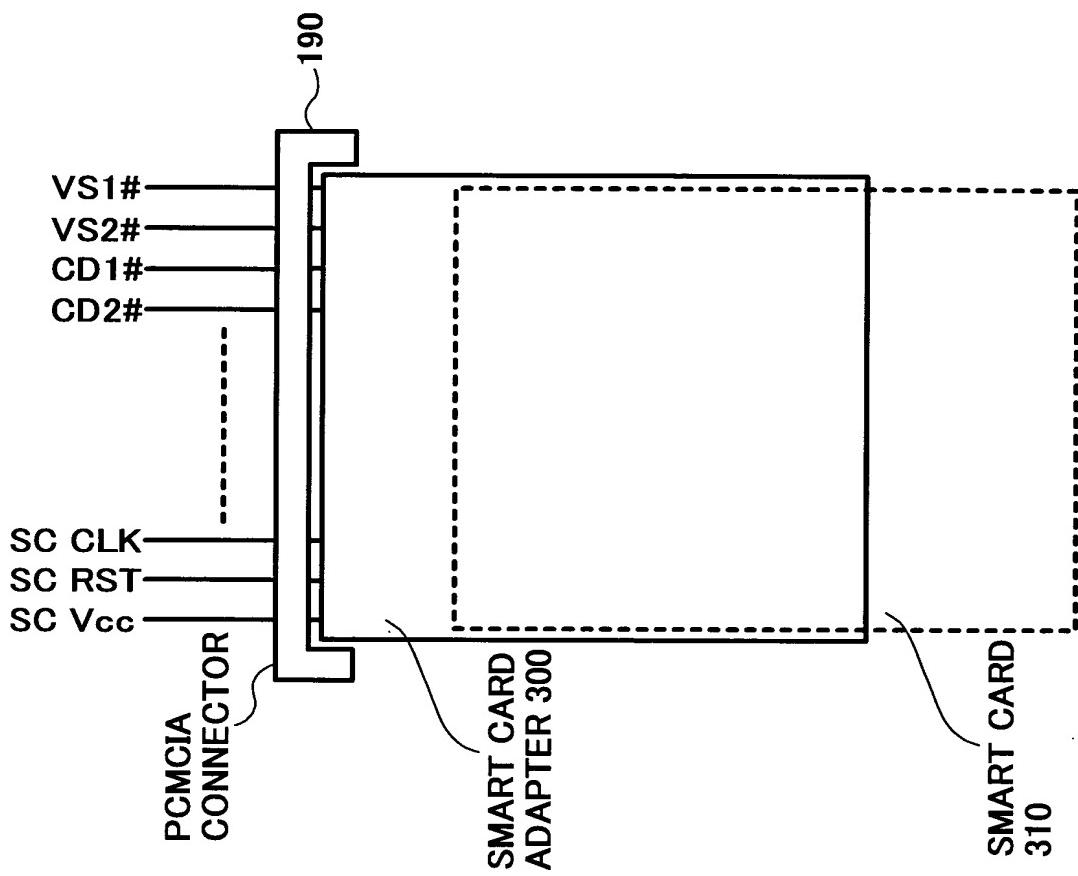
**FIG. 1**



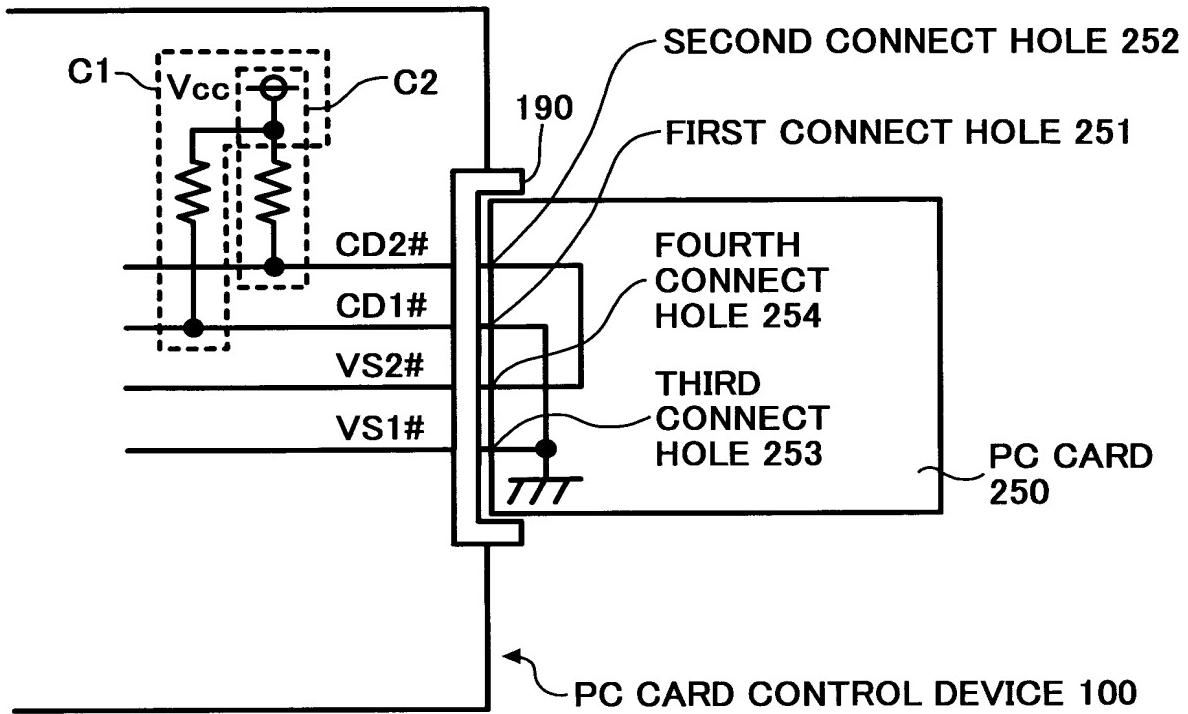
**FIG. 2A**



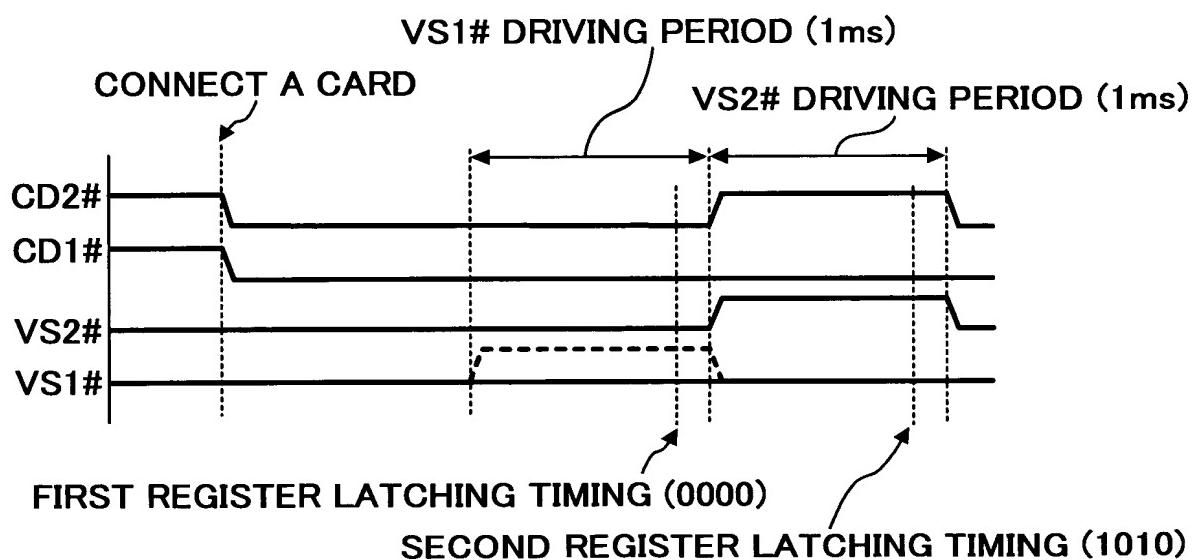
**FIG. 2B**



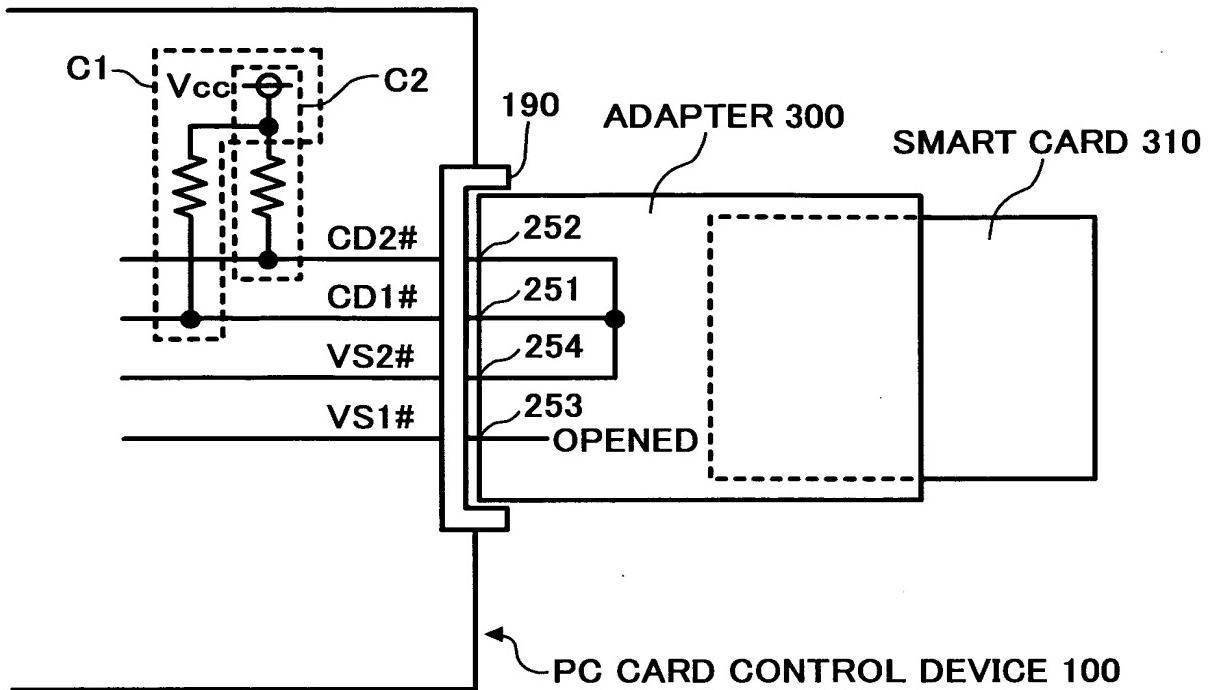
# FIG. 3A



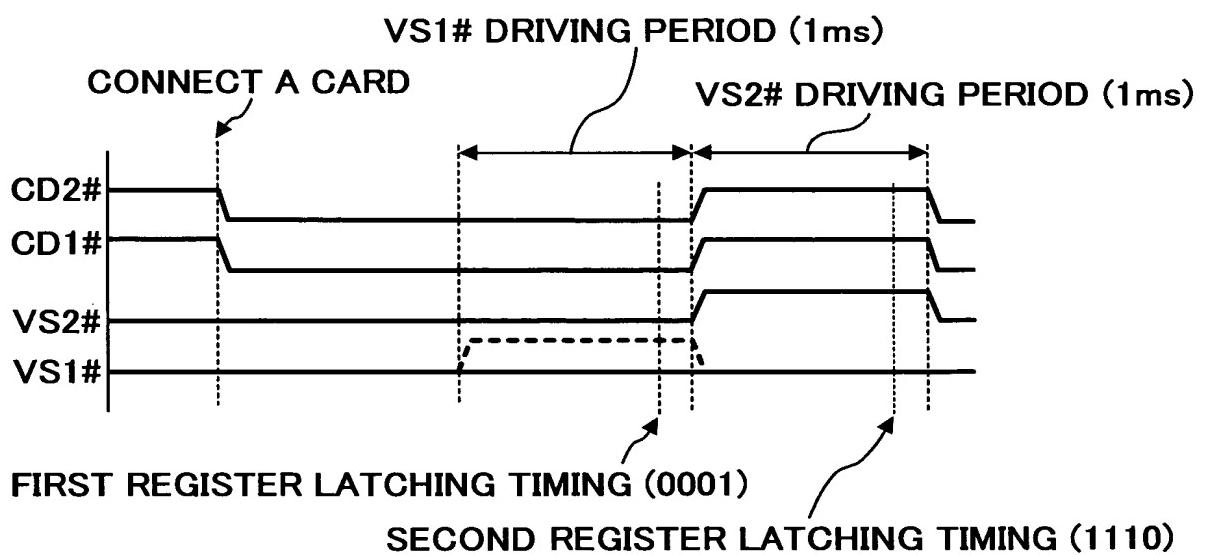
# FIG. 3B



## FIG. 4A



## FIG. 4B



# FIG. 5A

FIG. 5

FIG. 5A	FIG. 5B
FIG. 5C	FIG. 5D

NO.	CD2# FLOWING SECOND CONNECT HOLE	CD1# FLOWING FIRST CONNECT HOLE	VS2# FLOWING FOURTH CONNECT HOLE
1	GROUNDED	GROUNDED	GROUNDED
2	GROUNDED	GROUNDED	GROUNDED
3	GROUNDED	GROUNDED	OPENED
4	GROUNDED	GROUNDED	OPENED
5	GROUNDED	CONNECTED WITH THIRD CONNECT HOLE	GROUNDED
6	GROUNDED	CONNECTED WITH THIRD CONNECT HOLE	OPENED

**FIG. 5B**

VS1# FLOWING THIRD CONNECT HOLE	TYPE OF CARD		FIRST REGISTER STATE	SECOND REGISTER STATE
	INTERFACE	DRIVING VOLTAGE V		
GROUNDDED	16BIT	5V, 3.3V, X. XV	0000	0000
OPENED	16BIT	X. XV	0001	0000
GROUNDDED	16BIT	5V, 3.3V	0000	0010
OPENED	16BIT	5V	0001	0010
CONNECTED WITH FIRST CONNECT HOLE	RESERVED		0101	0000
CONNECTED WITH FIRST CONNECT HOLE	CARDBUS	3.3V	0101	0010

**FIG. 5C**

7	CONNECTED WITH FOURTH CONNECT HOLE	GROUNDED	CONNECTED WITH SECOND CONNECT HOLE
8	CONNECTED WITH FOURTH CONNECT HOLE	GROUNDED	CONNECTED WITH SECOND CONNECT HOLE
9	GROUNDED	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH FIRST CONNECT HOLE
10	GROUNDED	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH FIRST CONNECT HOLE
11	CONNECTED WITH THIRD CONNECT HOLE	GROUNDED	GROUNDED
12	CONNECTED WITH THIRD CONNECT HOLE	GROUNDED	OPENED

**FIG. 5D**

GROUNDED	CARDBUS	3.3V, X. XV	0000	1010
OPENED	CARDBUS	X. XV	0001	1010
GROUNDED	RESERVED		0000	0110
OPENED	CARDBUS	X. XV, Y. YY	0001	0110
CONNECTED WITH SECOND CONNECT HOLE	CARDBUS	3.3V, X. Y. YY	1001	0000
CONNECTED WITH SECOND CONNECT HOLE	CARDBUS	Y. YY	1001	0010

**FIG. 6A**

**FIG. 6A**

**FIG. 6B**

NO.	CD2# FLOWING SECOND CONNECT HOLE	CD1# FLOWING FIRST CONNECT HOLE	VS2# FLOWING FOURTH CONNECT HOLE
1	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH THIRD CONNECT HOLE	CONNECTED WITH SECOND CONNECT HOLE
2	CONNECTED WITH THIRD CONNECT HOLE	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH FIRST CONNECT HOLE
3	CONNECTED WITH THIRD CONNECT HOLE	CONNECTED WITH THIRD CONNECT HOLE	GROUNDED
4	CONNECTED WITH THIRD CONNECT HOLE	CONNECTED WITH THIRD CONNECT HOLE	OPENED
5	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH FIRST AND SECOND CONNECT HOLES
6	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH FIRST AND SECOND CONNECT HOLES

**FIG. 6B**

VS1# FLOWING THIRD CONNECT HOLE	TYPE OF CARD		FIRST REGISTER STATE	SECOND REGISTER STATE
CONNECTED WITH FIRST CONNECT HOLE	INTERFACE	DRIVING VOLTAGE V		
CONNECTED WITH SECOND CONNECT HOLE	NEW TYPE NO. 1		0101	1010
CONNECTED WITH FIRST AND SECOND CONNECT HOLES	NEW TYPE NO. 2		1001	0110
CONNECTED WITH FIRST AND SECOND CONNECT HOLES	NEW TYPE NO. 3		1101	0000
GROUNDED	NEW TYPE NO. 4		1101	0010
OPENED	NEW TYPE NO. 5		0000	1110
	NEW TYPE NO. 6		0001	1110

**FIG. 7A**

**FIG. 7**

**FIG. 7A**

**FIG. 7B**

**PC CARD IDENTIFYING PART 160**

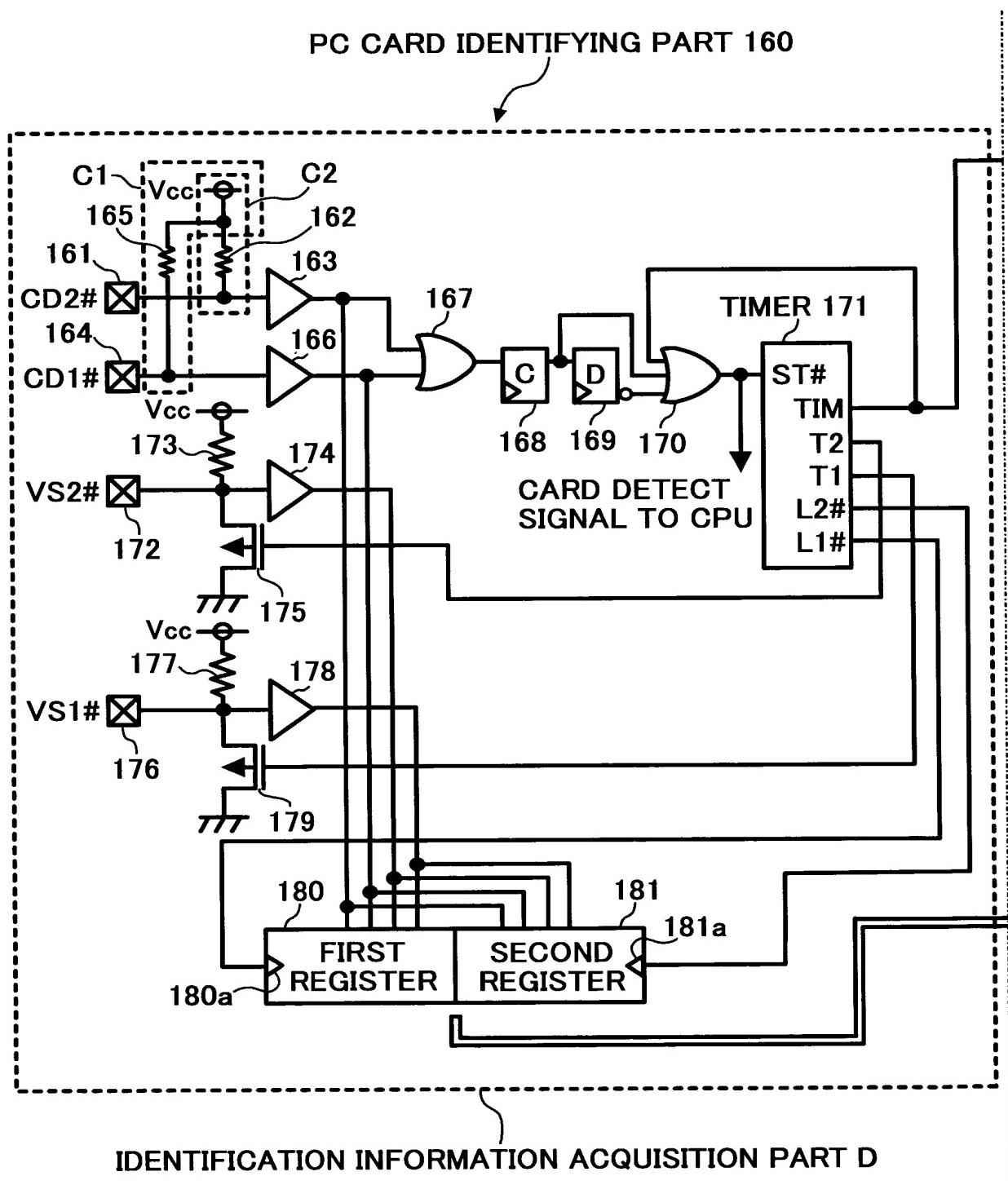
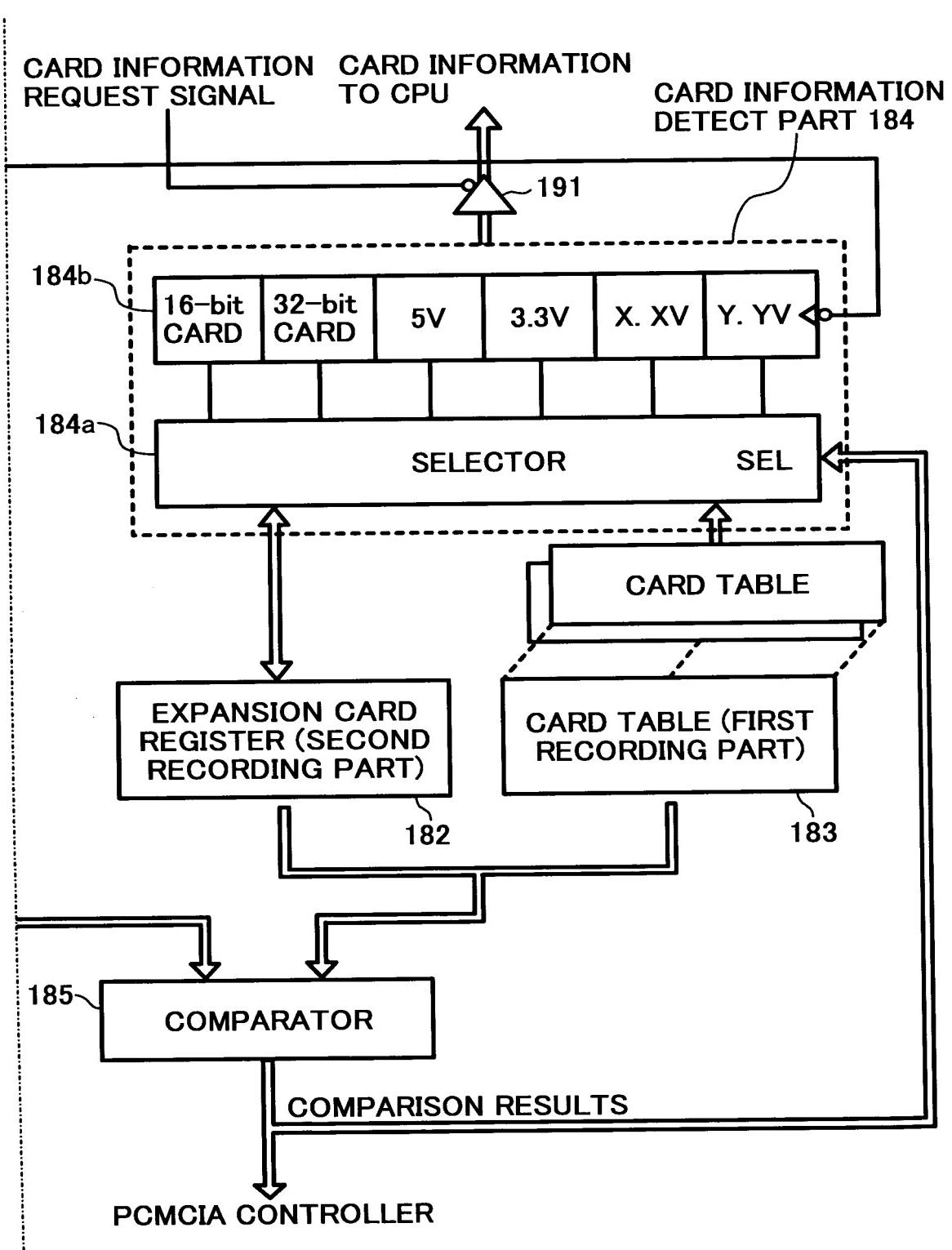
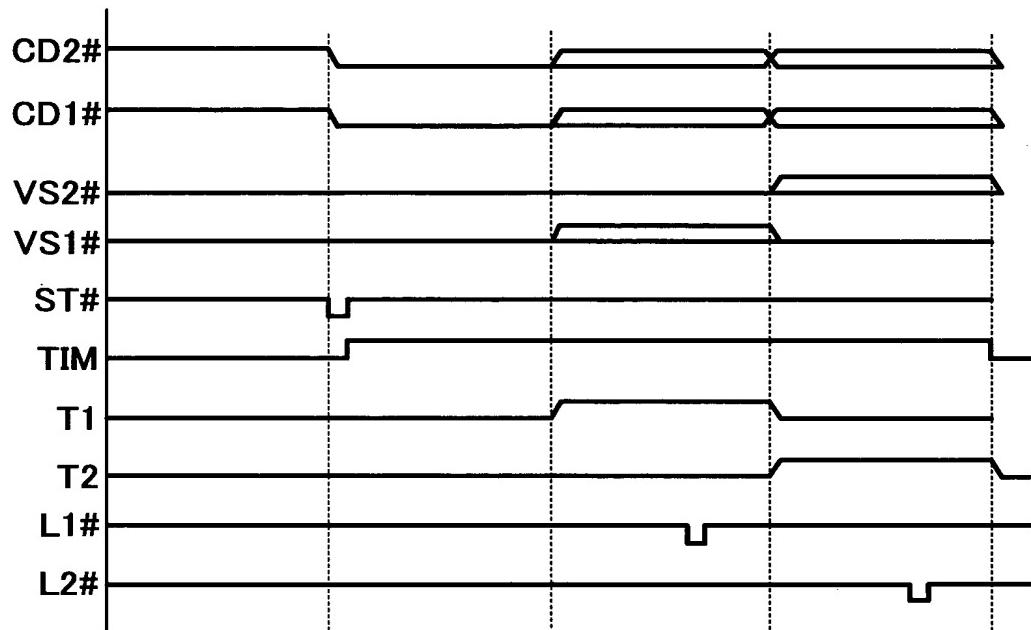


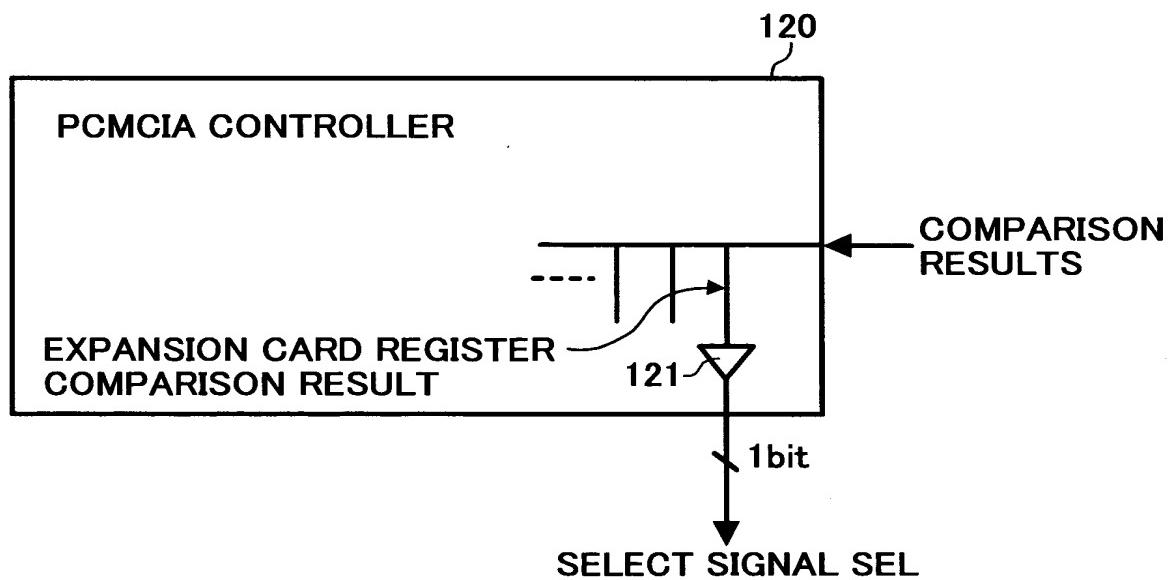
FIG. 7B



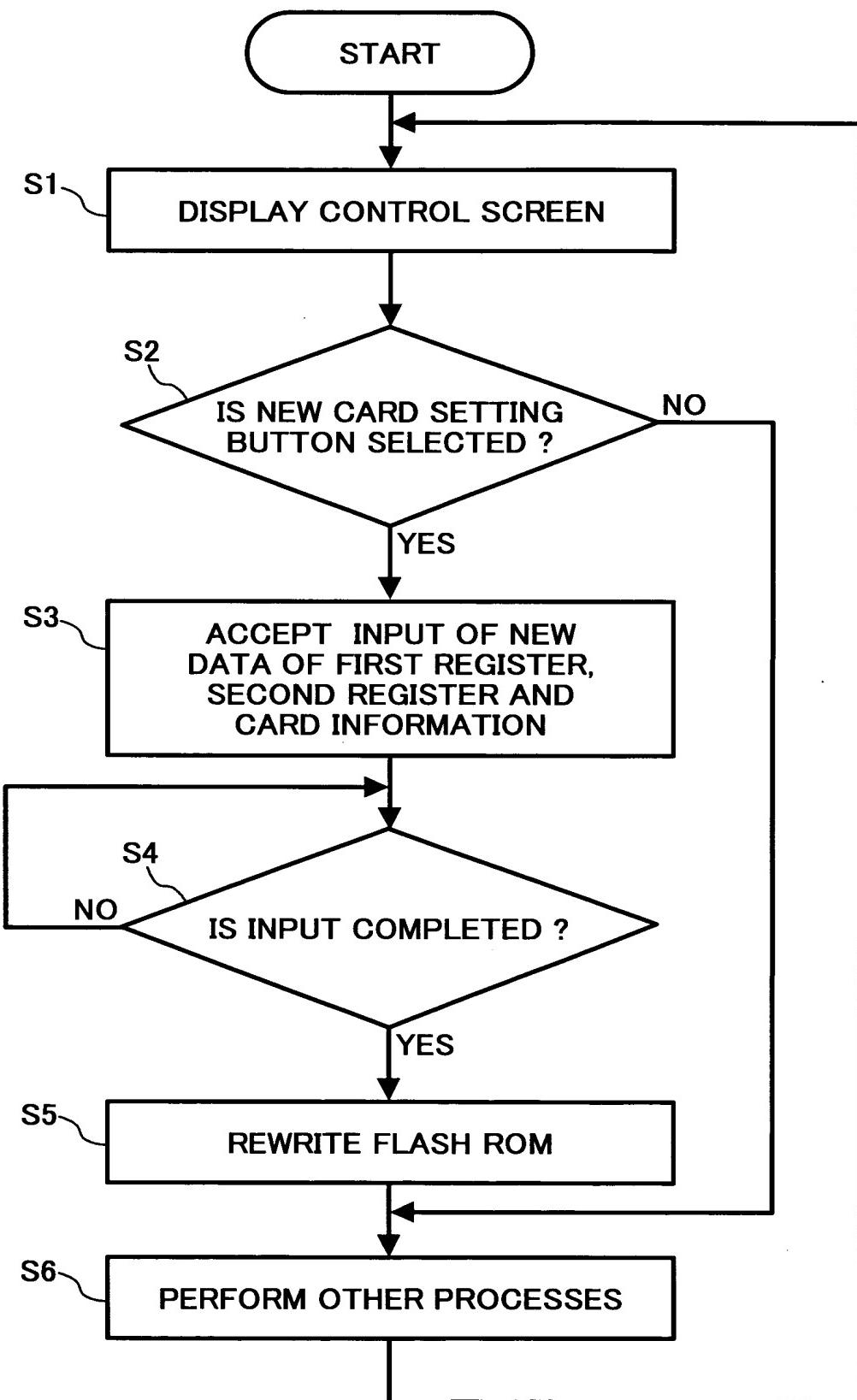
**FIG. 8**



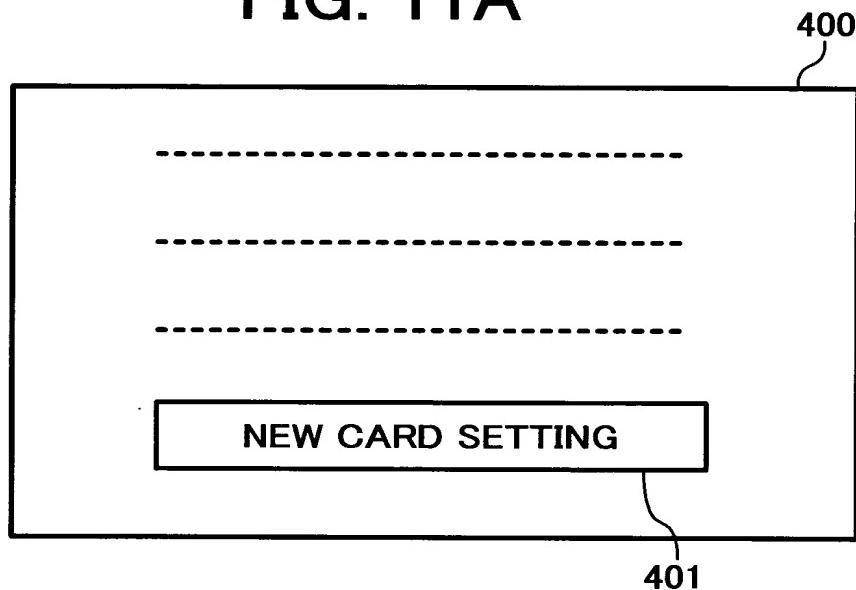
**FIG. 9**



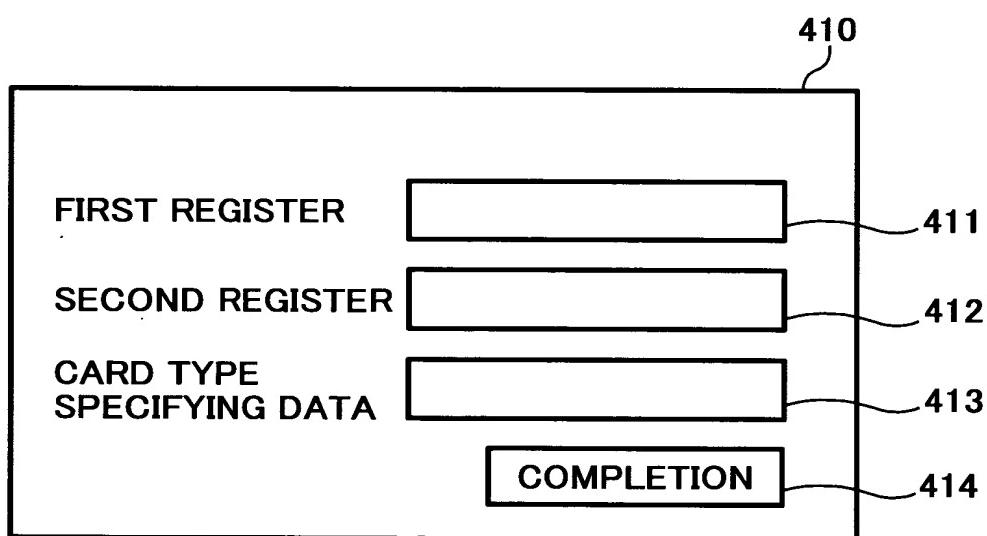
**FIG. 10**



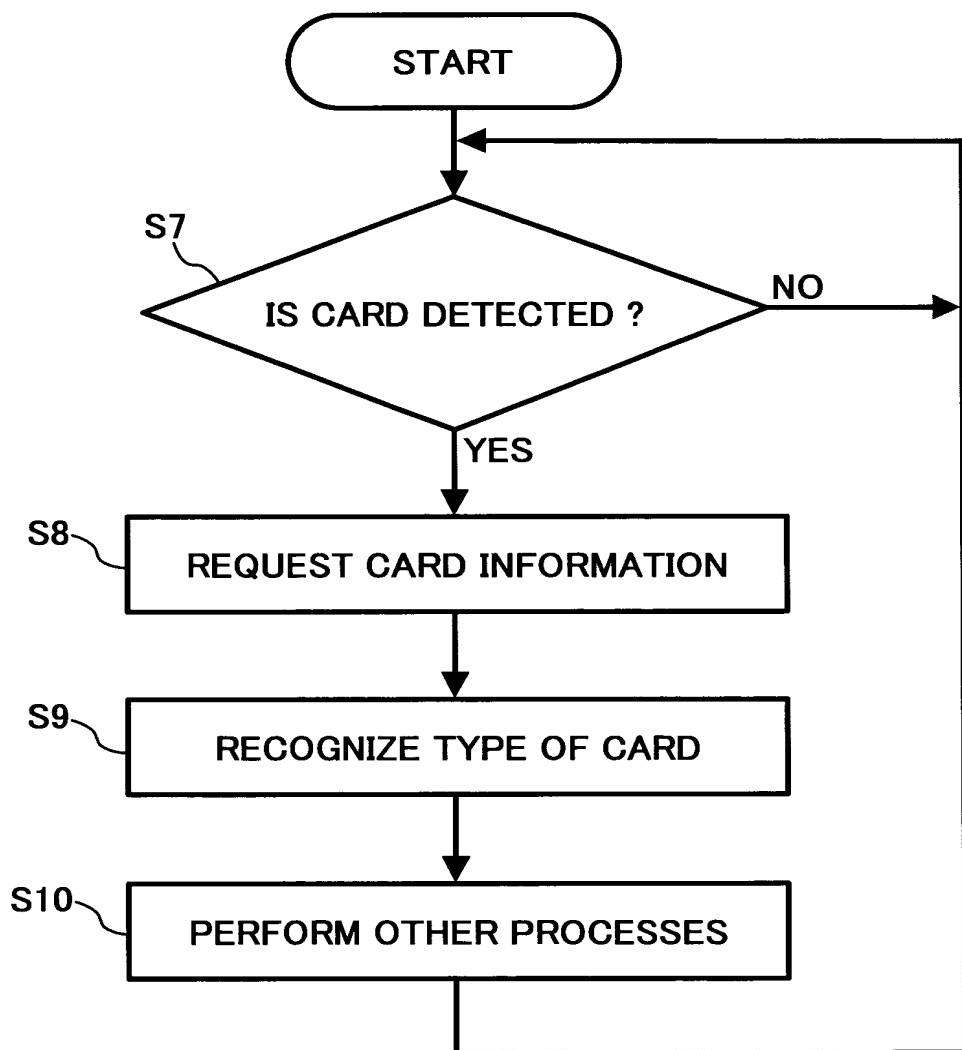
**FIG. 11A**



**FIG. 11B**

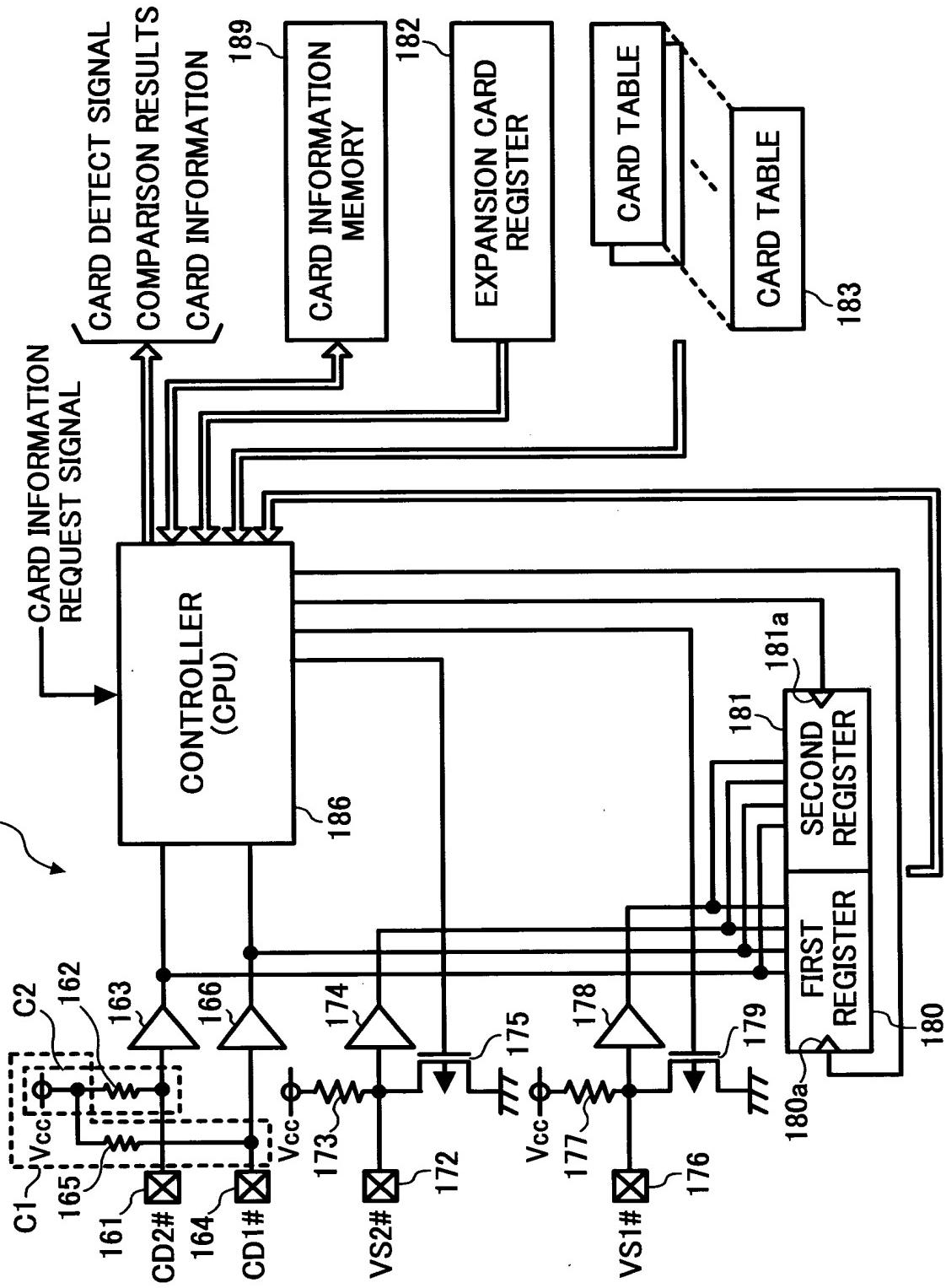


**FIG. 12**



**FIG. 13**

PC CARD IDENTIFYING PART 160a

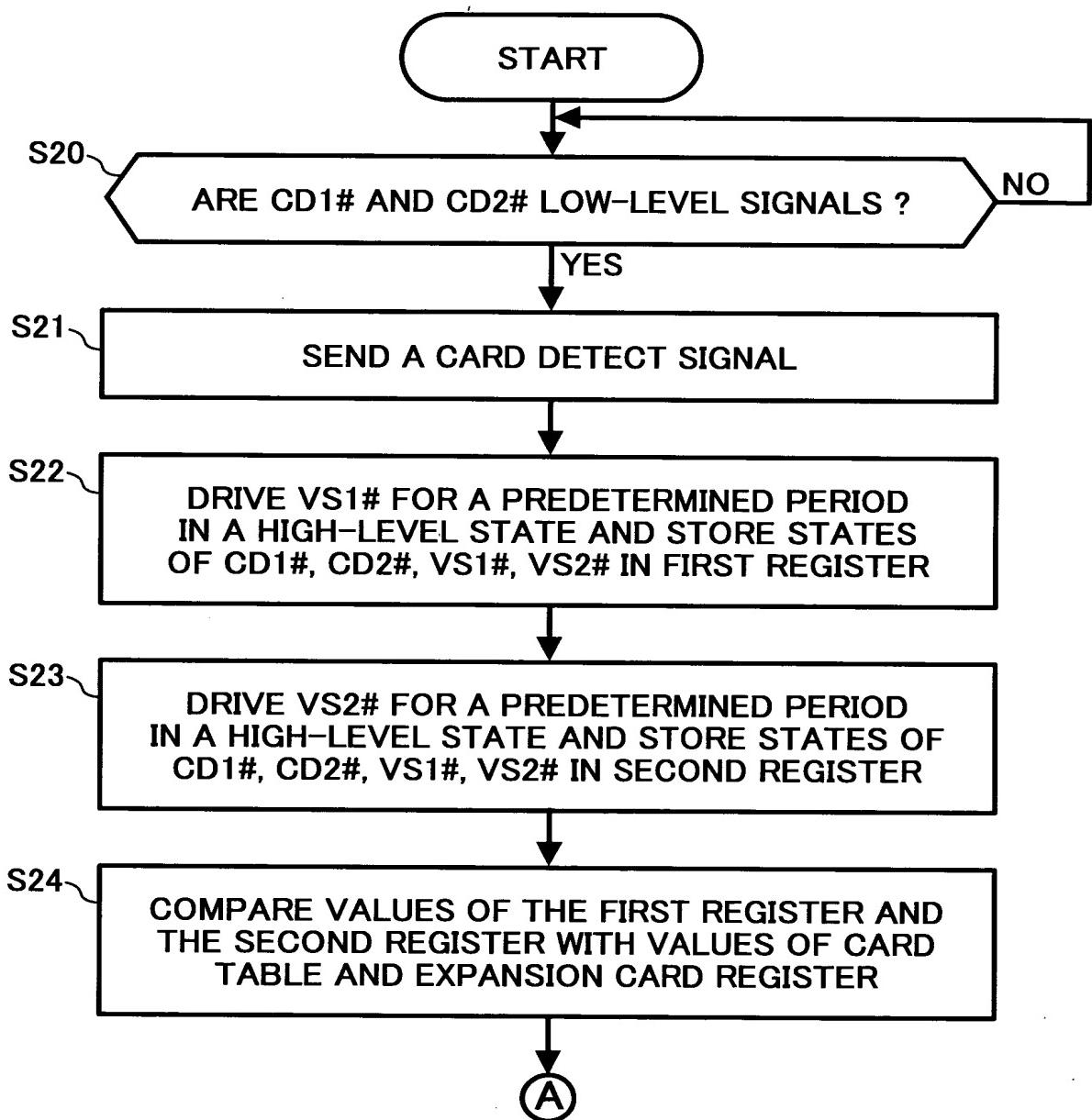


**FIG. 14A**

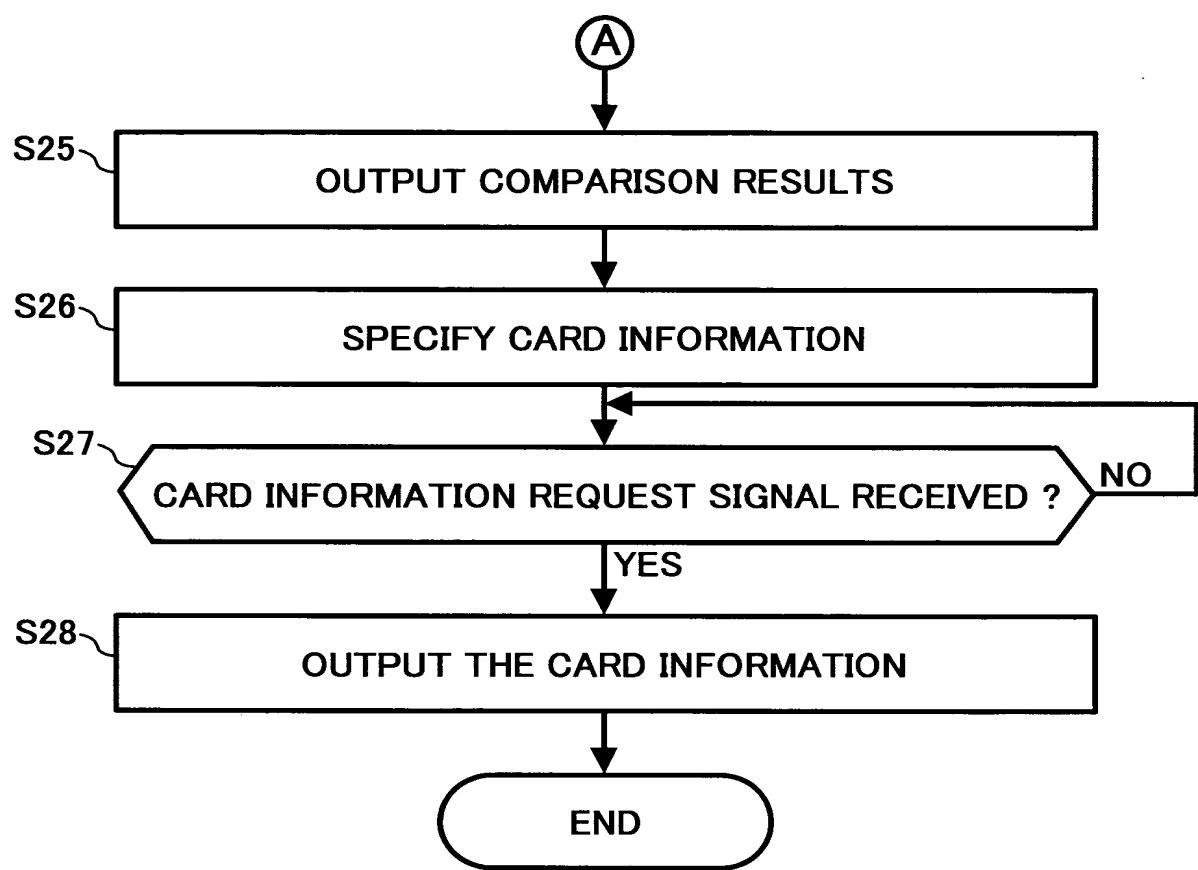
FIG. 14

**FIG. 14A**

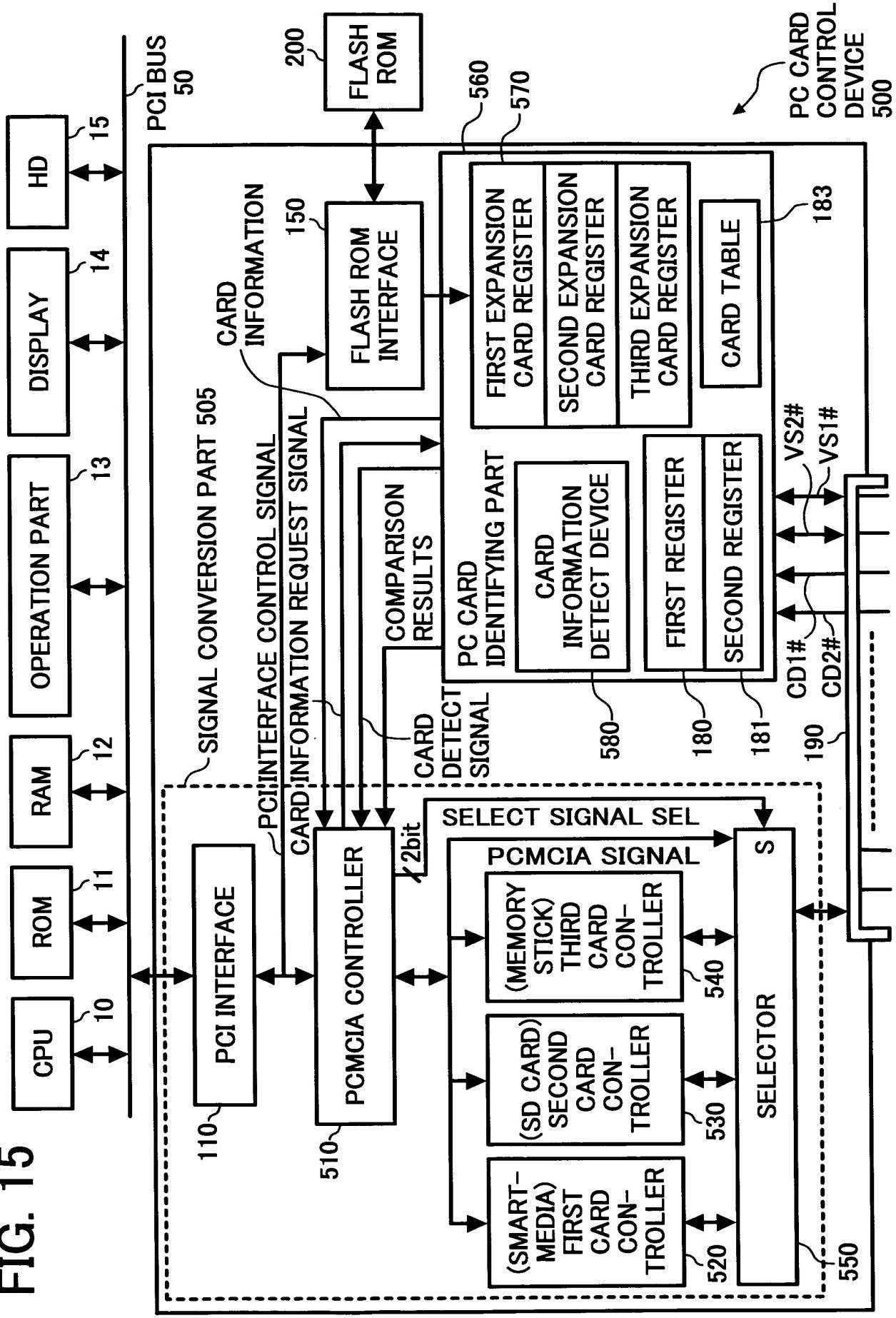
**FIG. 14B**



**FIG. 14B**



**FIG. 15**



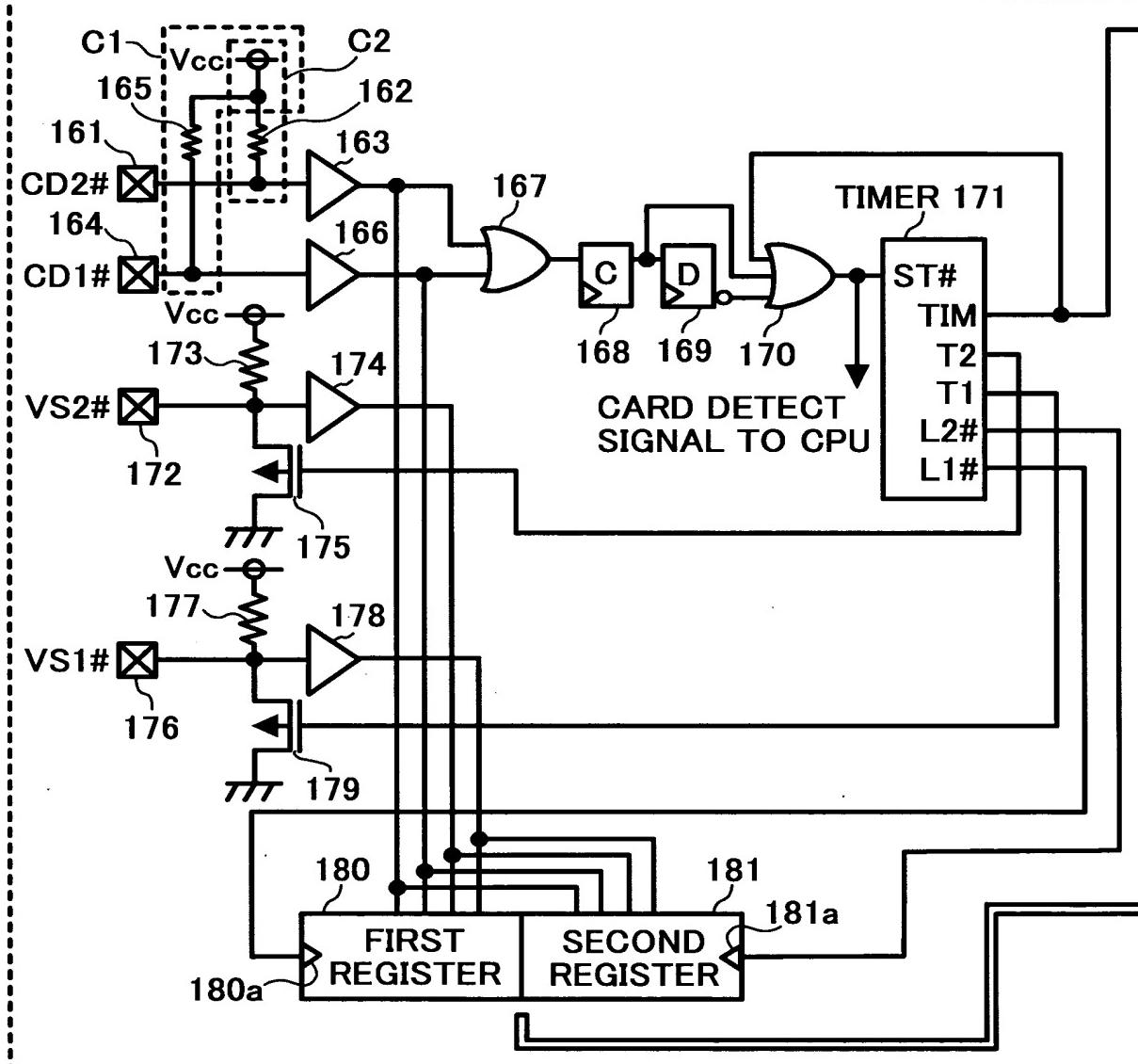
**FIG. 16A**

**FIG. 16**

**FIG. 16A**

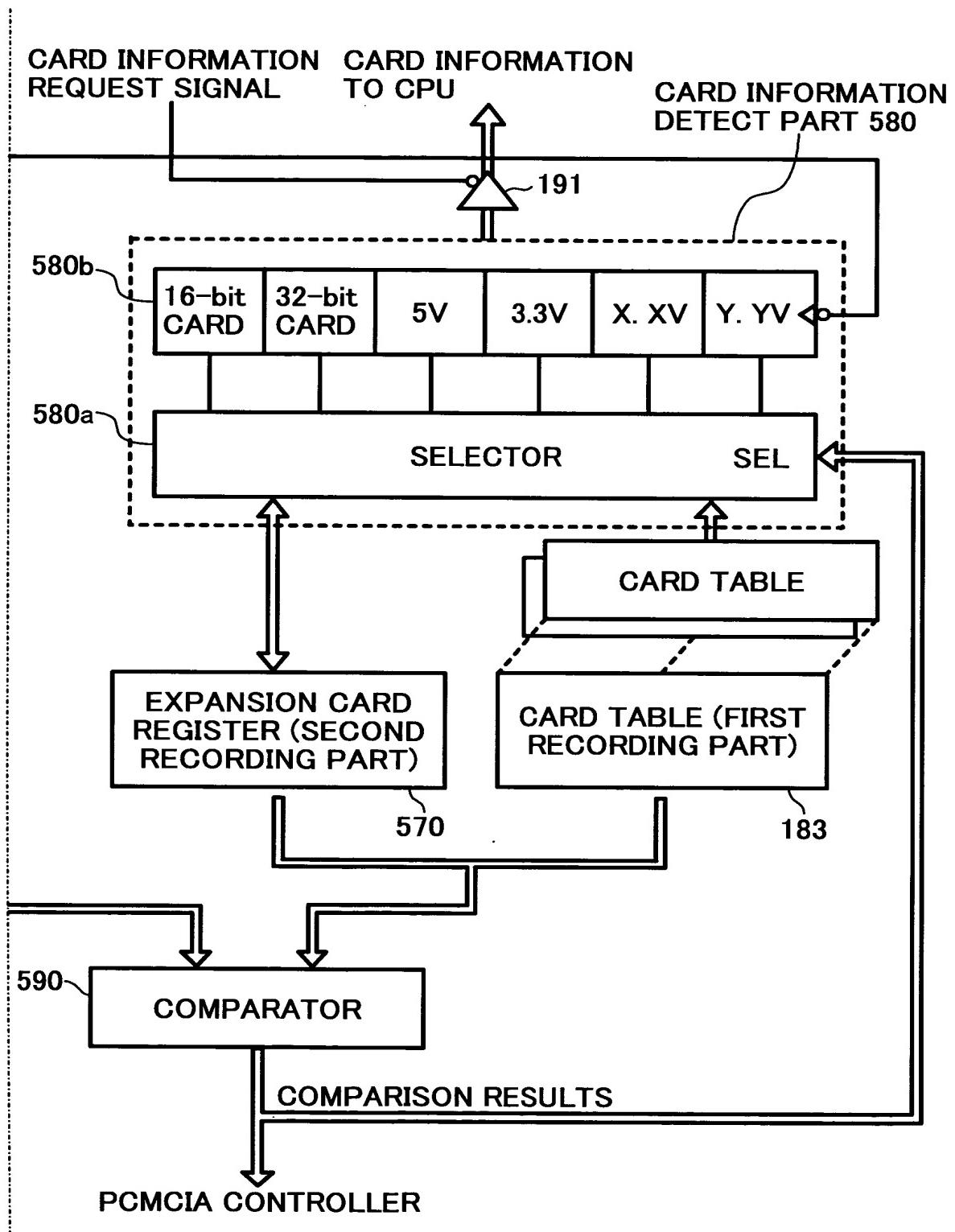
**FIG. 16B**

**PC CARD IDENTIFYING PART 560**



**IDENTIFICATION INFORMATION ACQUISITION PART D**

**FIG. 16B**



**FIG.17**

